

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Nam-Jung HER, et al.

Serial No.: 10/803,792 Examiner: Radosevich, Steven D.

Filed: March 17, 2004 Group Art Unit: 2138

Confirmation No.: 5596

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING A NUMBER
OF DATA OUTPUT PINS CAPABLE OF SELECTIVELY PROVIDING
OUTPUT SIGNALS AND TEST METHOD THEREOF

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Responsive to the Office Action dated June 29, 2006, please amend the application as follows.

Specification amendments begin on page 2.

Claims begin on page 4.

Remarks begin on page 6.